Accomplishments in Zhejiang University

• Product NanoScope
  – Accurate and fast lithographical process modeling
  – Silicon vs. layout verification
    • Defect check and distortion check
  – Design for manufacture
    • Enabling proximity-effect aware layout design

• Product NanoLib
  – Timing and power characterization of cell and simple IP
    • Automatic stimuli generation and functional verification
    • Distributed computing and data reduction for speed, accuracy and size
    • IR drop aware table modeling